

S20 3	41	S201 and controller with microcontroller	USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/05/05 13:13
S20 4	66	S201 and (cdma or wcdma or w-cdma)	USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/05/05 13:13

S18	0	("10" with "20" with "40" with "80") and wcdma	USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2003/06/24 15:45
S19	2	("10" with "20" with "40" with "80") and w-cdma	USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2003/06/24 15:46
S20	2	"6064662".pn.	USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2003/06/27 08:48
S21	398	partiti\$5 with symbols	USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2003/06/27 08:49
S22	63	(partiti\$5 with symbols) and data adj1 rate	USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2003/06/27 16:32
S23	111	reduc\$3 near emission\$1 with frequenc\$2	USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2003/06/27 16:34
S24	5	(reduc\$3 near emission\$1 with frequenc\$2) and (dmt or ofdm)	USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2003/06/27 16:35
S25	29374	cpu with address	USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2004/02/19 14:32
S26	540	cpu with address adj1 generator	USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2004/02/19 14:32
S27	1	(cpu with address adj1 generator) and cdma	USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2004/02/19 14:32
S20 1	3904	((buffer or ram) with (samples)) and segments	USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/05/05 13:10
S20 2	762	S201 and controller	USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/05/05 13:10